

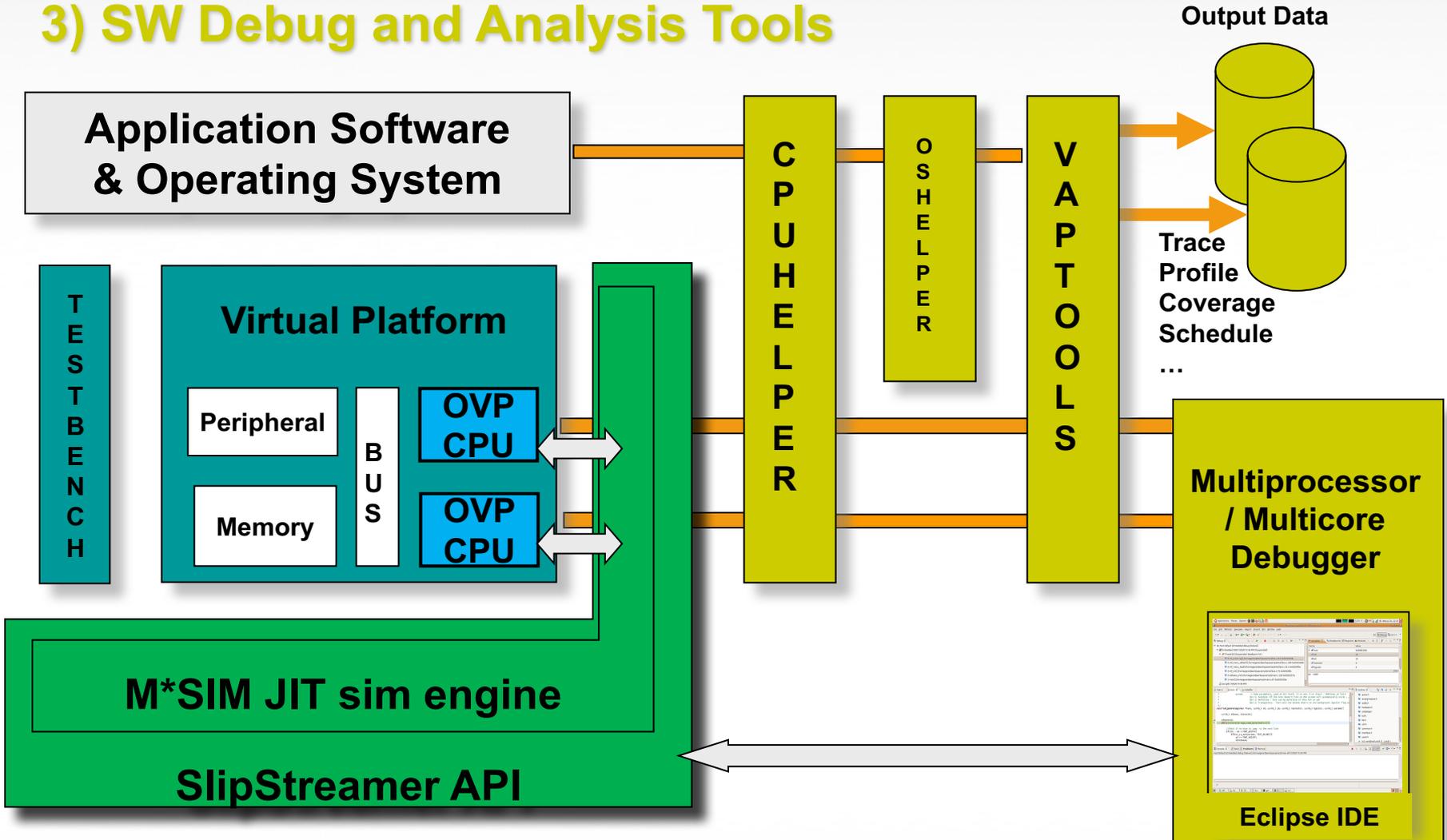


Virtual Platforms for RISC-V Software Development

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Key Imperas Differentiators:

- 1) CPU Models
- 2) Simulation Engines
- 3) SW Debug and Analysis Tools



Support for

- Imperas is a member of RISC-V Foundation
- Platforms / OS Combinations available for free download from Open Virtual Platforms (OVP) website, www.OVPworld.org
 - Bare metal application examples – processor + memory
 - Extendable Platform Kits (EPKs)
 - Operating systems on implementations of actual hardware platforms
- Processor models available as open source downloads
 - RV32I, RV32G
 - RV64I, RV64G
 - Others under development (contact Imperas for schedule)
- Models can be used in C, C++, SystemC, and TLM2 platforms and with 3rd party simulators/environments

Imperas Users Benefit From Improved Software Quality, and Reduced Schedules & Cost



- Key technologies: 170+ processor model library, fastest simulator, software VAP and debug tools
- Solutions for embedded use cases: custom CPU, semiconductor vendors, embedded systems developers

