Copyright © 2019 QuickLogic, Inc.



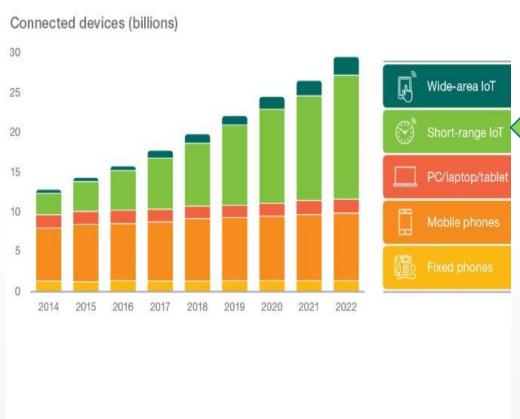
# Enabling AI on Low Power Endpoint Devices

Tim Saxe





### Growth in resource constrained devices

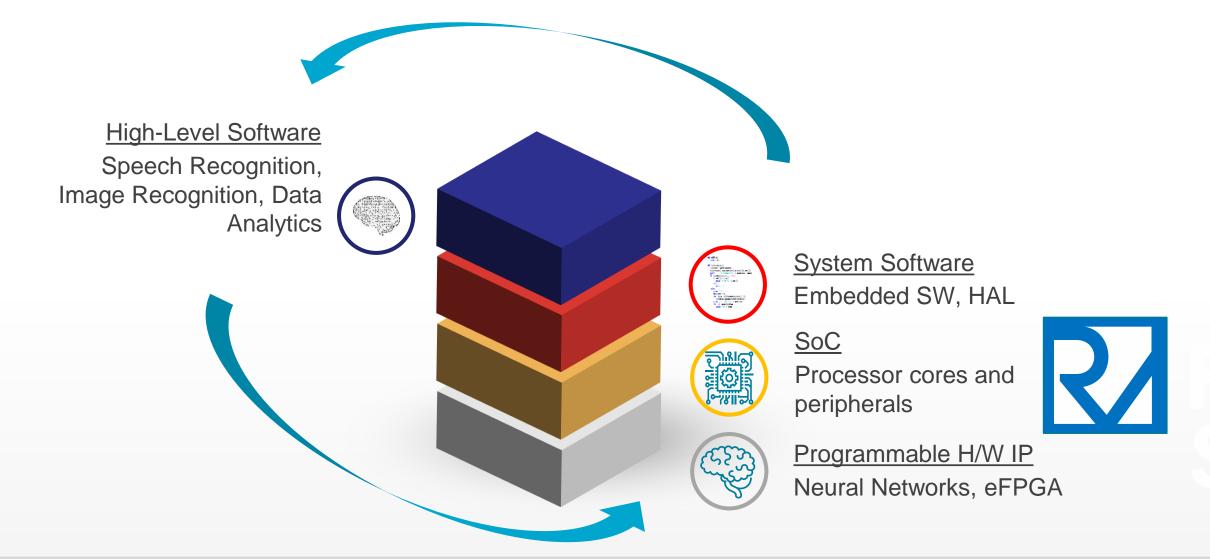


"Within the wide-area IoT segment, two distinct sub-segments with different requirements have emerged: massive and critical applications.

Massive IoT connections are characterized by high connection volumes and small data traffic volumes, low cost devices and low energy consumption."

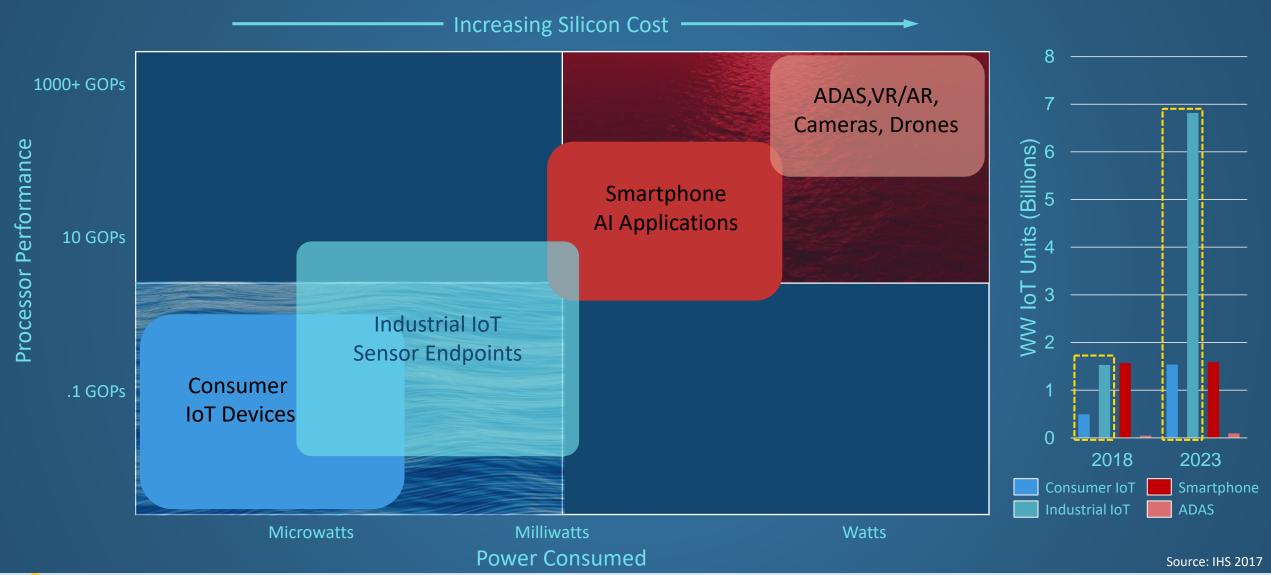
https://www.ericsson.com/en/mobility-report/internet-of-things-forecast

# Silicon is Necessary but not Sufficient: Need Full-Stack



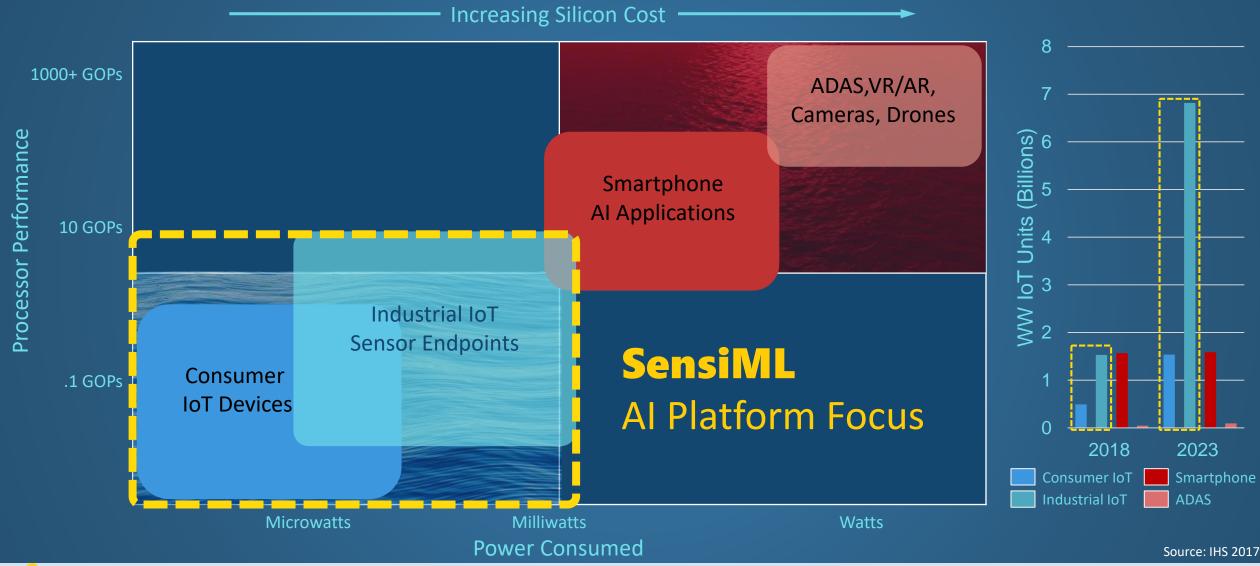


# Endpoint and Edge AI: Platform Segments



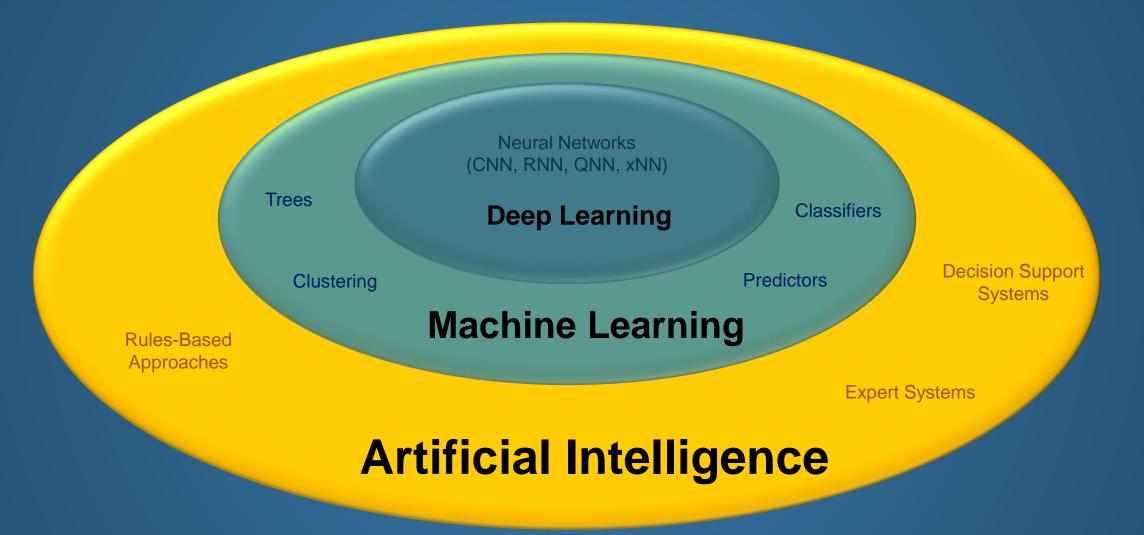


# Endpoint and Edge AI: Platform Segments



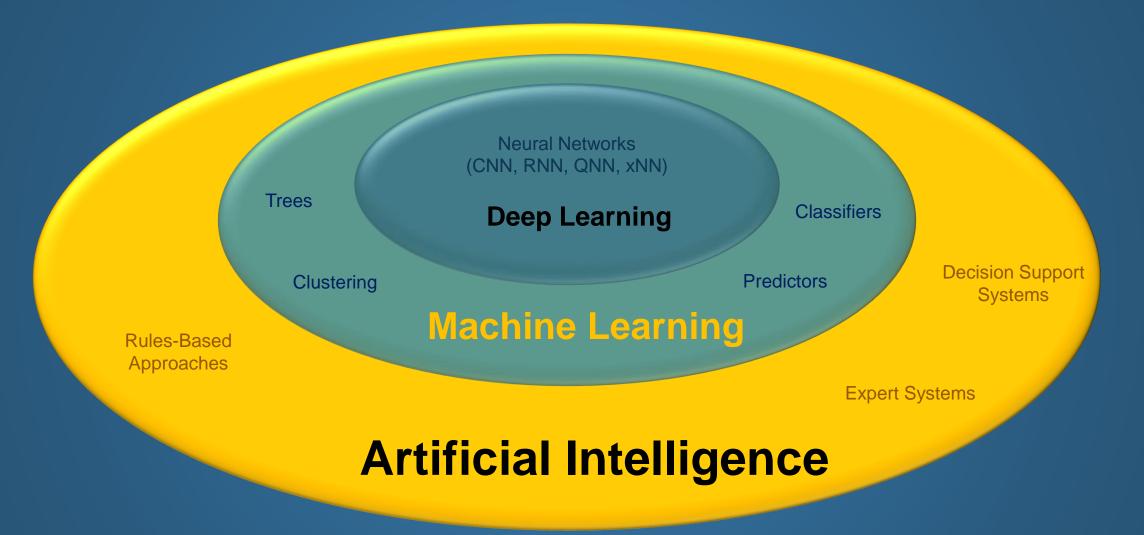


# A High-Level Taxonomy of Al



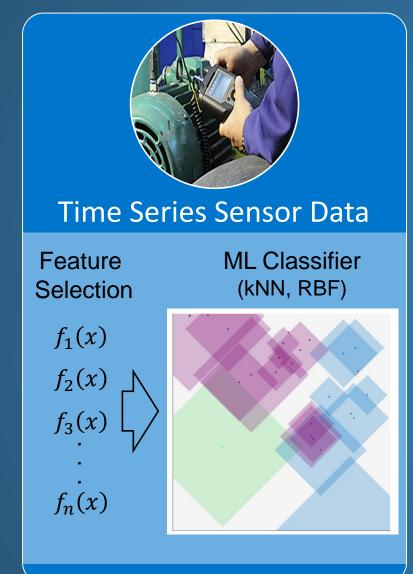


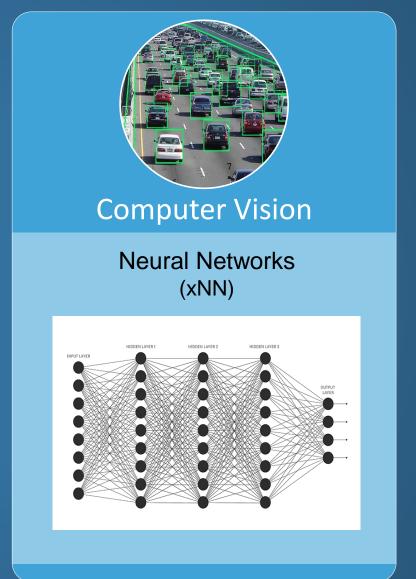
# A High-Level Taxonomy of Al





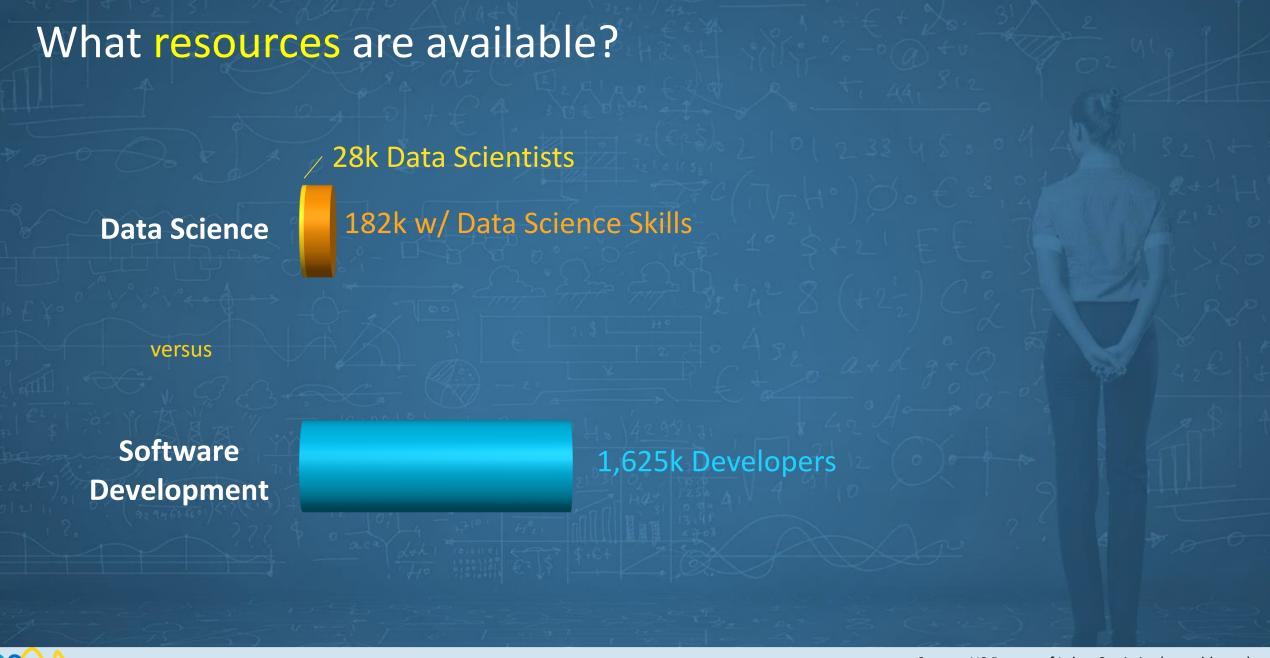
### Which AI method best fits the application?







Tiny ML



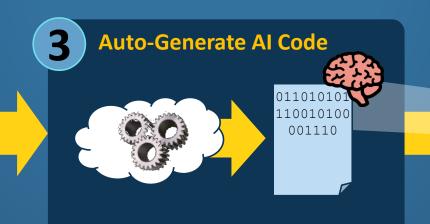


# SensiML Makes Building Intelligent Endpoints Practical



Democratizing the application of AI to Millions of Software Developers and Billions of Endpoint IoT Devices









## Solution lies in open standards and automation

### Foundation of AI is quality data

- SensiML Data Capture Lab
  - Open interface to control data acquisition
  - Open interface to collect data
  - Open source data warehouse to share data
  - Powerful tool for data set management
  - Powerful computer assisted labelling
    - Minimize the drudgery
    - Maximize the insights

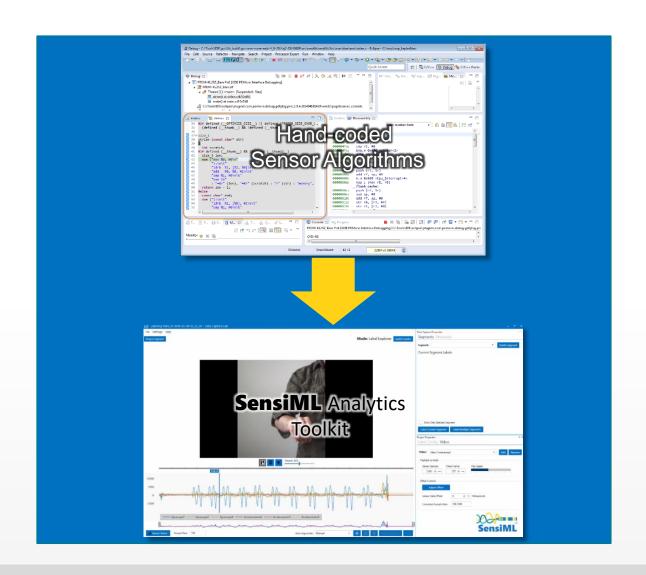


Encompasses 4Cs of Labeling Training Data: Collection, Cleansing, Collaboration, Curation

### ...and more automation

### Purpose of AI is classification

- SensiML Analytic Toolkit
  - Understands the resource limitations of the target device
  - Explores feature extraction/classification algorithm tradeoffs
    - Over 100 feature extractors optimized for resource constrained devices
    - Multiple algorithms from classical ML to Neural Nets
    - Open interface allows 3<sup>rd</sup> party additions
  - Eliminates the need for hand-coded algorithms
    - Speeds PoC days, not weeks
    - Speeds development weeks, not months



# Supported Platforms



**QuickLogic - QuickAl Accelerated Al Platforms** 



**Development Kit** 



ST - STM32 & SensorTile ARM Cortex-M Series **Processors** 





**Nordic Semiconductor** nRF52 & Nordic Thingy IoT **Sensor Kit** 



Raspberry Pi 3/3Bs



**Arnold Testbed -**PULPissimo + eFPGA

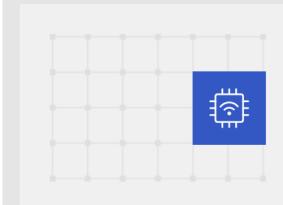


**Freedom Aware** (coming soon)

# **Templates**

#### **Freedom Aware**

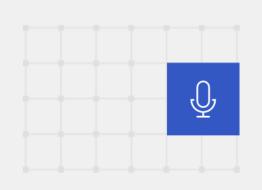
# Ultra-low power solutions optimized for consumer and industrial IoT applications.



#### MCU for IoT

Designed for embedded IoTs, microcontrollers, wearables and more

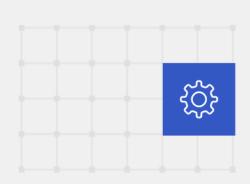
Learn More



#### Always-On Voice Processor

Optimized for smart devices and more

Learn More



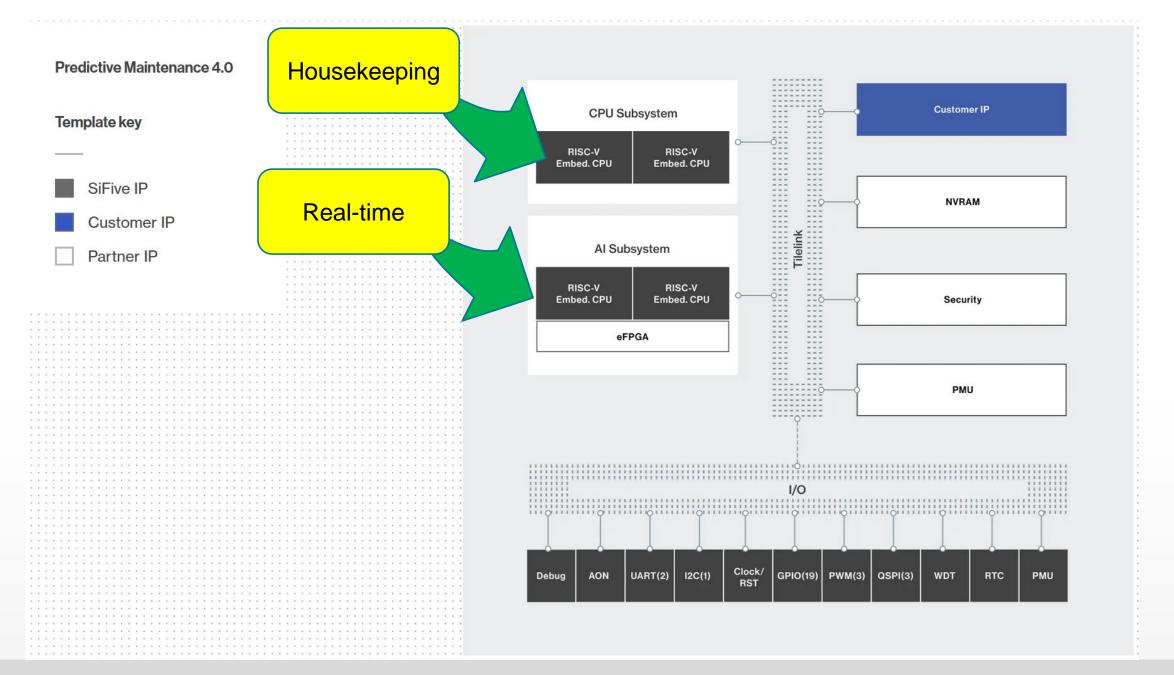
#### **Predictive Maintenance 4.0**

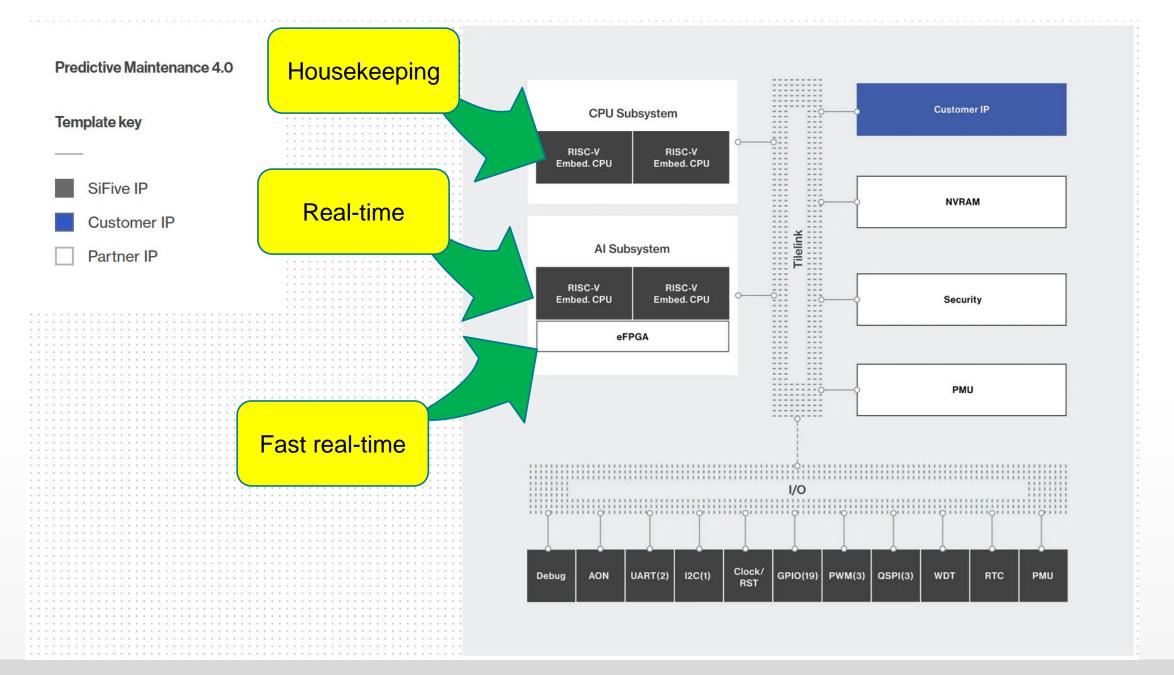
Designed to support digital and analog sensors

Learn More

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#### **Predictive Maintenance 4.0 Customer IP CPU Subsystem** Tilelink Template key RISC-V RISC-V Embed. CPU Embed. CPU SiFive IP **NVRAM Customer IP** Al Subsystem Partner IP RISC-V RISC-V Embed. CPU Embed. CPU Security eFPGA **PMU** .... Clock/ UART(2) 12C(1) QSPI(3) PMU AON GPIO(19) PWM(3) WDT RTC Debug





# Thank you!